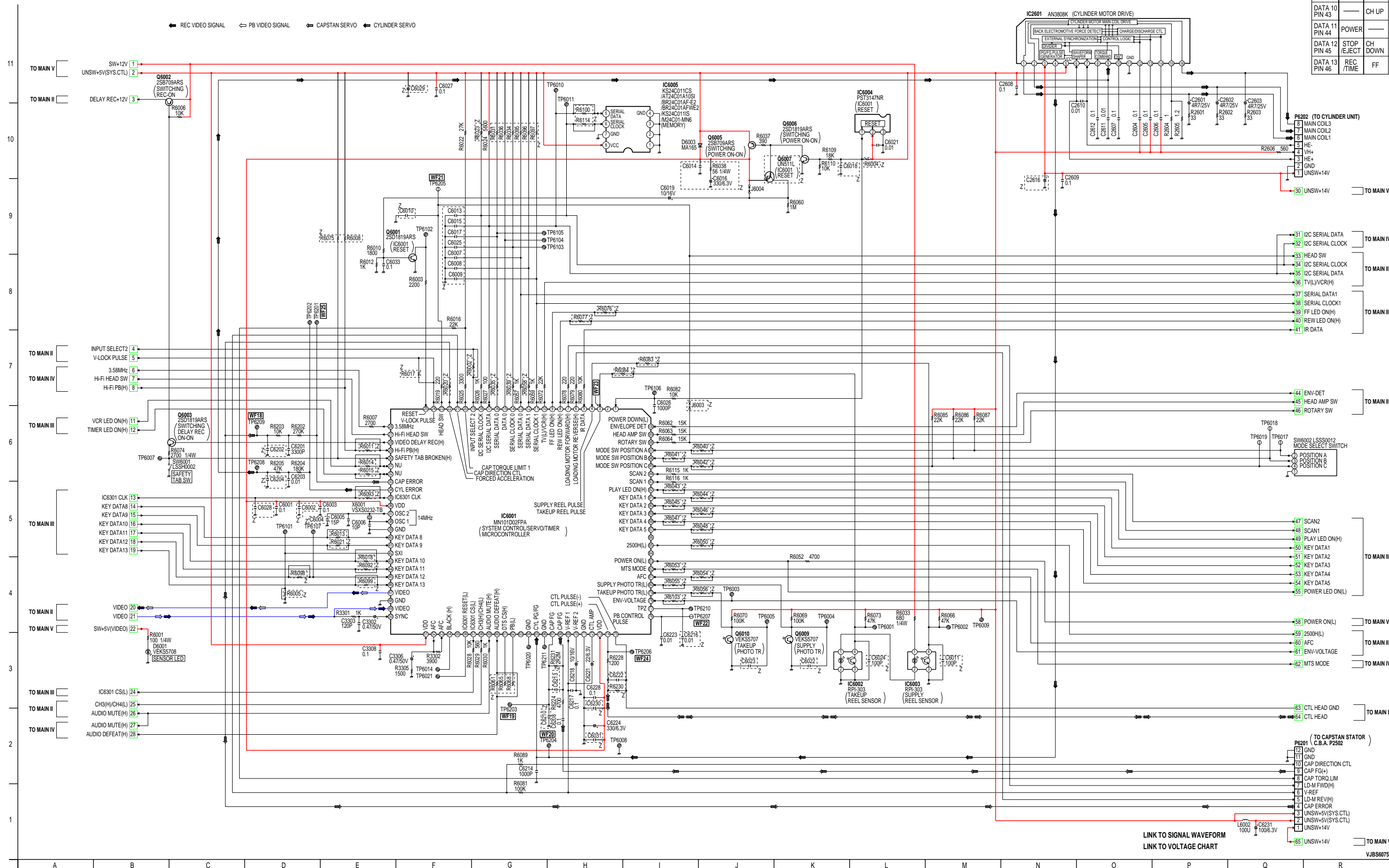
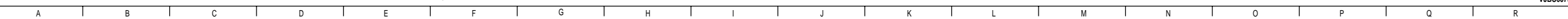


E:
FOR SCHEMATIC DIAGRAM AND CIRCUIT BOARD LAYOUT NOTES
REFER TO BEGINNING OF SCHEMATIC SECTION.

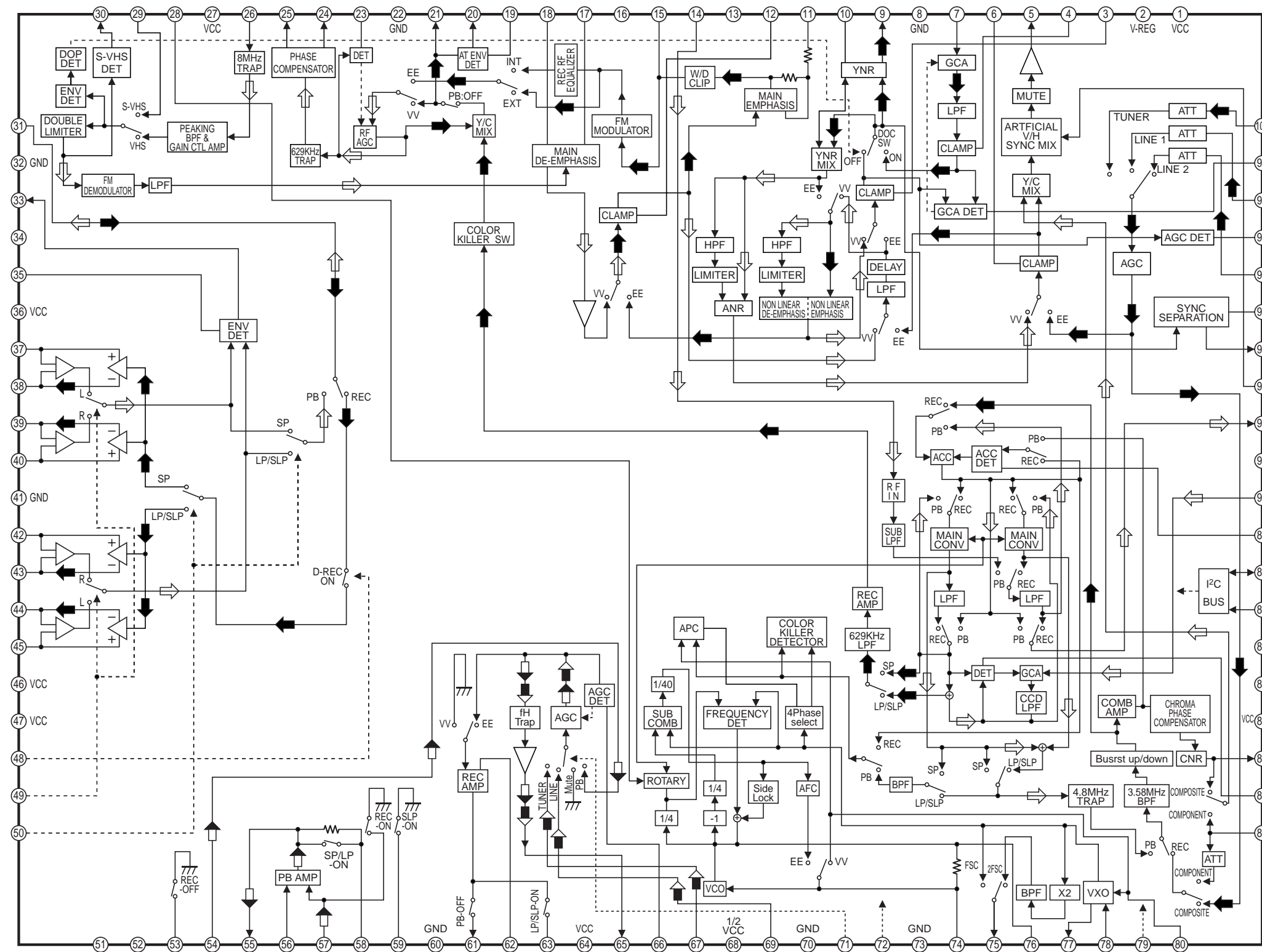
KEY MATRIX CHART		
OUT IN	SCAN 1 PIN 93	SCAN 2 PIN 94
DATA 8 PIN40	VCR/TV	REW
DATA 9 PIN 41	—	PLAY
DATA 10 PIN 43	—	CH UP
DATA 11 PIN 44	POWER	—
DATA 12 PIN 45	STOP /EJECT	CH DOWN
DATA 13 PIN 46	REC /TIME	FF





V.JBS607

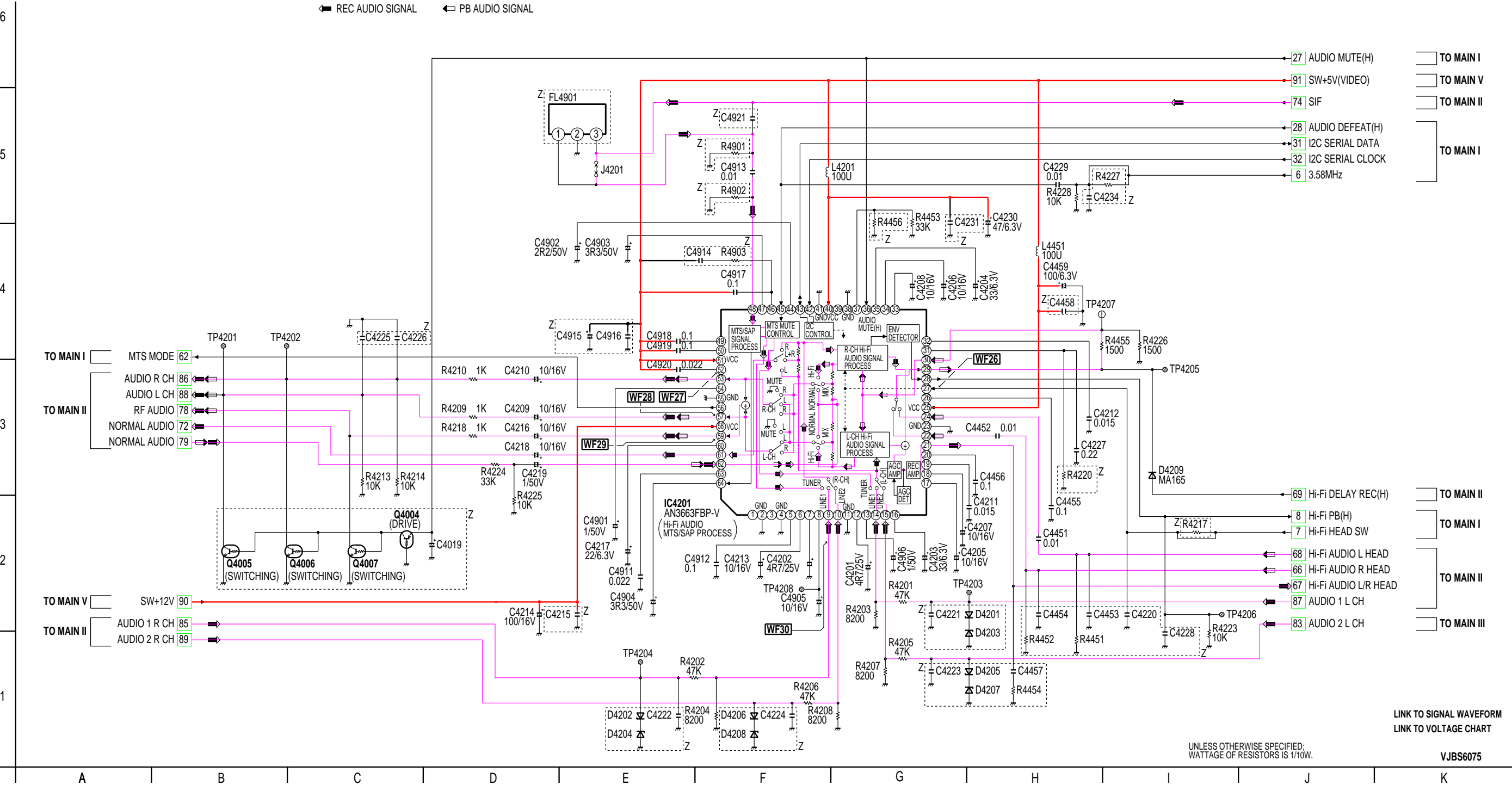
IC3001 VIDEO/AUDIO SIGNAL PROCESS / HEAD AMP IC- DETAIL BLOCK DIAGRAM, MN13400A



MAIN IV (Hi-Fi) SCHEMATIC DIAGRAM

NOTE: PARTS ENCLOSED IN DASHED LINES MARKED "Z" ARE NOT USED.


NOTE: FOR SCHEMATIC DIAGRAM AND CIRCUIT BOARD LAYOUT NOTES, REFER TO BEGINNING OF SCHEMATIC SECTION.



MAIN V (POWER SUPPLY) SCHEMATIC DIAGRAM

NOTE:
PARTS ENCLOSED IN DASHED LINES MARKED "Z" ARE NOT USED.

NOTE:
FOR SCHEMATIC DIAGRAM AND CIRCUIT BOARD LAYOUT NOTES,
REFER TO BEGINNING OF SCHEMATIC SECTION.

IMPORTANT SAFETY NOTICE:
COMPONENTS IDENTIFIED BY THE SIGN  HAVE
SPECIAL CHARACTERISTICS IMPORTANT FOR SAFETY.
WHEN REPLACING ANY OF THESE COMPONENTS,
USE ONLY THE SPECIFIED PARTS.

